

AMENDMENTS TO THE CLAIMS

1. (currently amended) A method for transforming a digital voltage signal into an analog voltage signal to reduce spikes by utilizing a digital-to-analog converter, the digital-to-analog converter comprising a plurality of digit circuits, ~~wherein each digit circuit respectively corresponds to a bit of the digital voltage signal,~~ the method comprising:
receiving the digital voltage signal;
setting the digit circuit corresponding to a predetermined bit of the digital voltage signal closest to an output module; and
outputting an analog voltage signal corresponding to the digital voltage signal;
wherein the predetermined bit of the digital voltage signal is the bit with least signal variation among the bits of the digital voltage signal.
2. (original) The method of claim 1, wherein each digit circuit comprises a plurality of transistors.
3. (original) The method of claim 1, wherein the digital-to-analog converter is an R-string digital-to-analog converter.
4. (original) The method of claim 1, wherein the output module of the digital-to-analog converter comprises a loading resistor for impedance matching.

5. (currently amended) A digital-to-analog converter for transforming a digital voltage signal into an analog voltage signal, the digital-to-analog converter comprising:
- 5 a receiving circuit for receiving the digital voltage signal;
- a plurality of digit circuits electrically connected to the receiving circuit for transforming the digital voltage signal into an
- 10 analog voltage signal corresponding to the digital voltage signal, ~~each digit circuit corresponding to a bit of the digital voltage signal~~; and
- an output module comprising a loading resistor for
- 15 impedance matching electrically connected to a the digit circuits;
- ~~that corresponds to a bit wherein one of the digit circuits with least signal variation among the bits of the digital voltage signal for~~
- 20 ~~outputting the transformed analog voltage signal is closest to the output module.~~ ~~the output module comprising a loading resistor for impedance matching.~~
- 25 6. (original) The digital-to-analog converter of claim 5, wherein each digit circuit comprises a plurality of transistors.
7. (original) The digital-to-analog converter of claim
- 30 5 being an R-string digital-to-analog converter.
8. (original) The digital-to-analog converter of claim

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5 further comprising a reference circuit electrically connected to the plurality of digit circuits for providing a reference voltage for the plurality of digit circuits.

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